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Docket No.: 4363P004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re the Application of:

ARASH HASSIBI, ET AL.

Application No.: 09/843,486

Filed: April 25, 2001

For: **Optimal Simultaneous Design and Floorplanning
of Integrated Circuit**

Art Group: 2811

Examiner: Quang D. Vu

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PETITION FOR EXTENSION OF TIME PURSUANT TO 37 C.F.R. § 1.136(a)

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

In accordance with 37 C.F. R. § 1.136(a), Applicants for the above-identified application respectfully
Petition the Commissioner for a one (1) month extension of time, extending the period for response to December
07, 2002, from the Office Action dated August 07, 2002. The petition filing fee of \$110.00 and a Amendment and
Response to Office Action are attached.

If it should be determined that a longer extension of time is required to prevent this application from being
abandoned, please charge any additional fees to Deposit Account No. 02-2666. A copy of the Fee Transmittal is
enclosed for deposit account charging purposes.

Respectfully submitted,

BLAKELY, SOLOZOFF, TAYLOR & ZAFMAN LLP

Date:

12/9/02

Robert B. O'Rourke, Reg. No. 46,972

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I hereby certify that this correspondence is being deposited with the
United States Postal Service as First Class mail with sufficient
postage in an envelope addressed to: Assistant Commissioner for
Patents, Washington, D.C. 20231 on: December 09, 2002.

Janece Shannon
Janece Shannon

12-9-2002
12-09-02
Date

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